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OUR FILE:

85195-602 ADB

YOUR FILE:



March 9, 2004

U.S. Patent and Trademark Office
2011 South Clark Place
Customer Window, Mail Stop Patent Application
Crystal Plaza Two, Lobby, Room 1B03
Arlington, Virginia 22202
U.S.A.

Dear Sir:

RE: United States Patent Application
METHOD FOR FRACTIONAL RF SIGNAL SYNTHESIS WITH PHASE MODULATION

Being duly authorized and appointed, the undersigned is filing the attached application pursuant to the provisions of revised 37 CFR 1.41(c) and 1.53(b) on behalf of the inventor, whose full name and address are as follows:

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We attach a Declaration providing relevant details of this application. However this Declaration is NOT SIGNED. This application is being filed without a filing fee. An appropriate signed Declaration and the filing fee will be filed upon receipt of The Notice of Missing Parts.

Granting of a filing date is respectfully solicited.

Respectfully submitted,

Adrian D. Battison
Registration No. 31,726
ADB/II
Enc.(13)